

Fig.1

Status Normal: Primary origin and Primary Destination are operational, secondary Synchronization from Primary Destination: Revert nodes are idle Circuit Alarm Synchronization successful: Reroute Secondary Attempting Destination is synchronization with operational Primary Destination Synchronization successful: Reroute Synchronization fails Attempting synchronization with Secondary Destination

Fig.2

#### Tav.II

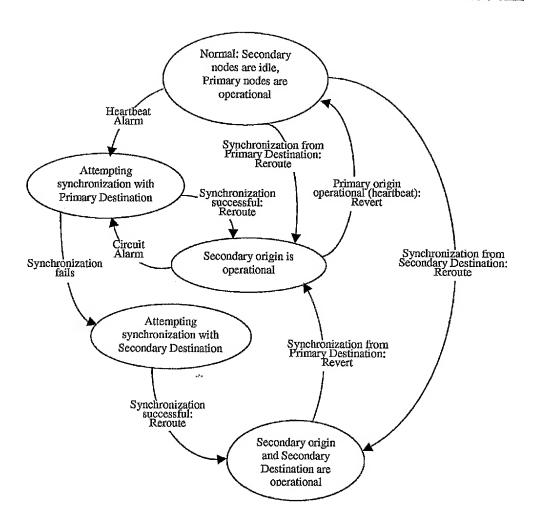


Fig.3

#### Tav.III

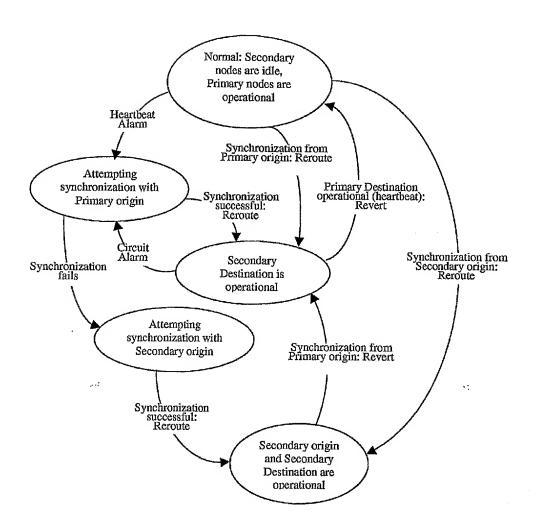


Fig.4

## Tav.IV

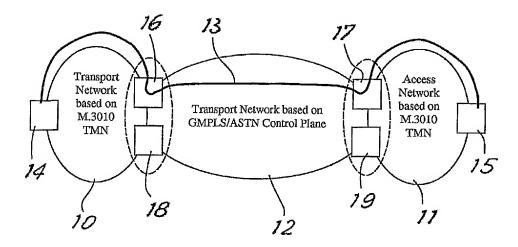


Fig.5

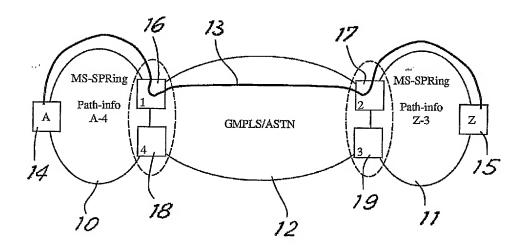


Fig.6

## Tav.V

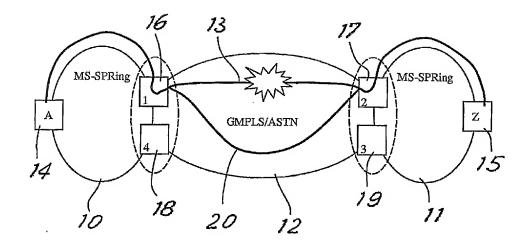
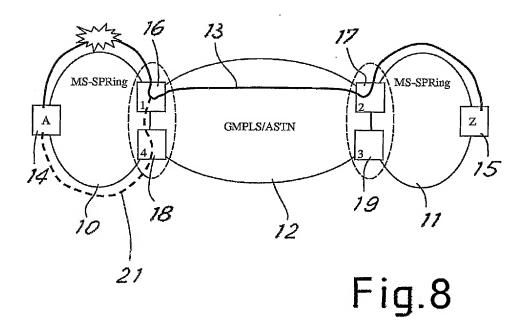


Fig.7



# Tav.VI

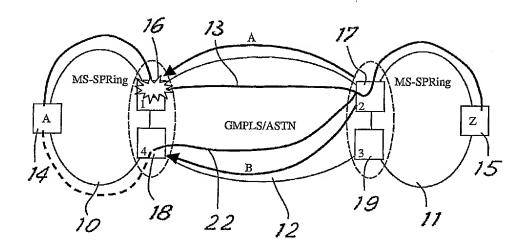


Fig.9

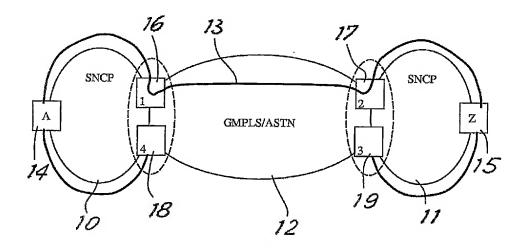


Fig. 10

## Tav.VII

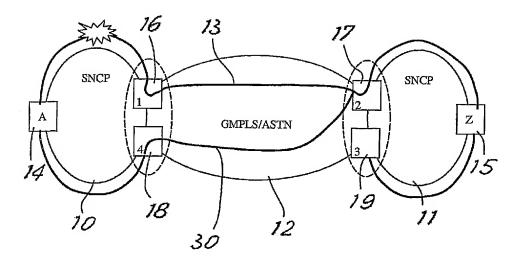


Fig.11

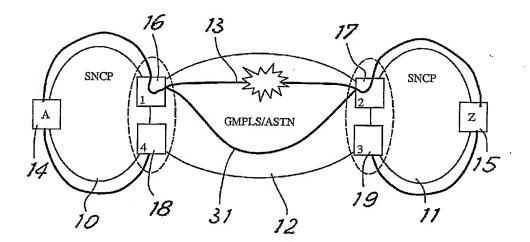


Fig.12

## Tav.VIII

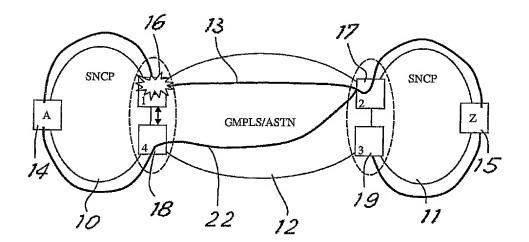


Fig. 13

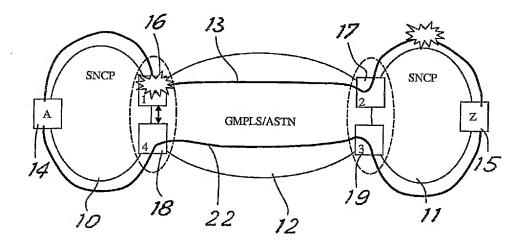


Fig. 14

# Tav.IX

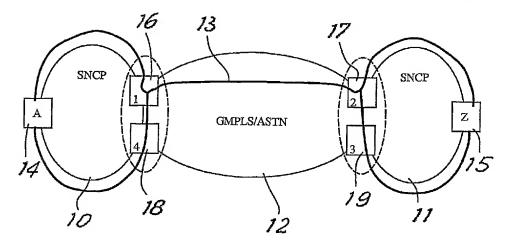


Fig. 15

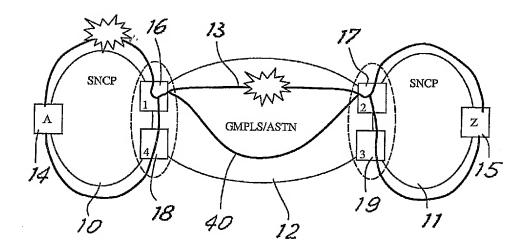


Fig.16

# Tav.X

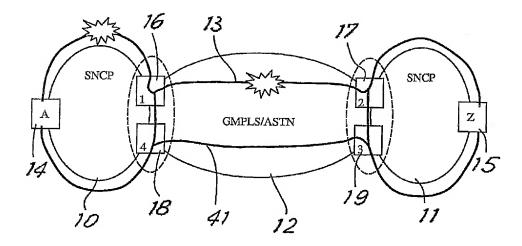


Fig. 17

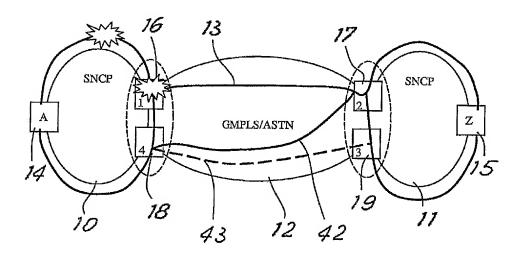


Fig.18

# Tav.XI

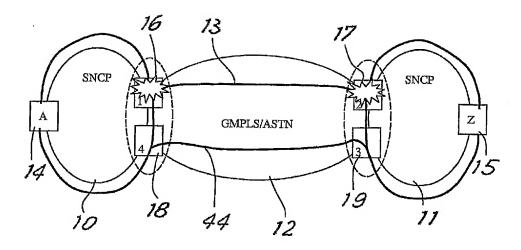


Fig. 19

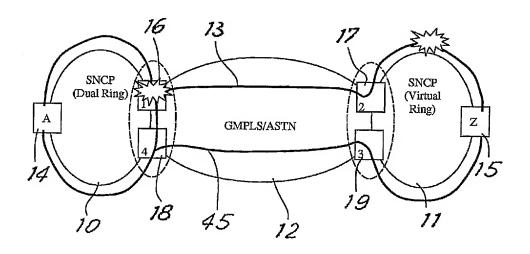


Fig.20

# Tav.XII

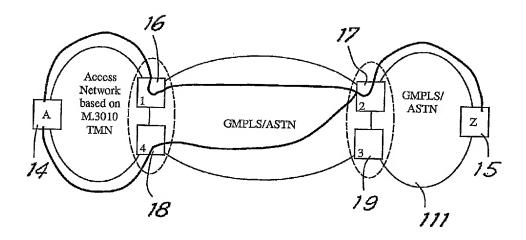


Fig.21

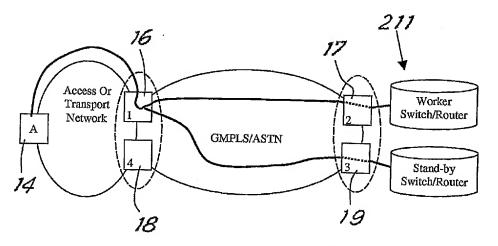


Fig.22